

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention

BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE

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First Named Applicant: Gilbert Wolrich
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Examiner:



Search string: (3373408 or 3478322 or 3792441 or 3913074 or 3940745 or 4130890 or 4392758 or 4400770 or 4514807 or 4523272 or 4724521 or 4745544 or 4777587 or 4866664 or 5073864 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or 5173897 or 5255239 or 5263169 or 5347648 or 5357617 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404464 or 5404482 or 5428809 or 5432918 or 5442756 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5481683 or 5487159 or 5517628 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or 20020053017 or 20020056037 or 20030041228 or 20030145159 or 20030191866 or 20040039895 or 20040054880 or 20040071152 or 20040073728 or 20040073778 or 20040098496 or 20040109369 or 20040205747).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

Examiner Name	Date